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Inventors: Kiyotaka Tsukada, Masaru Takada, and Kenji Chihara

Title: *Printed Circuit Board and Method of Manufacturing Same*

Priority: Japanese Application No. 10-192992; Filed July 8, 1998

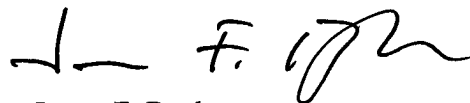
INFORMATION DISCLOSURE STATEMENT

DO/EO/US
Director of the U.S. Patent
and Trademark Office
Washington, D.C. 20231

Sir:

This information disclosure statement is being submitted simultaneously with the filing of the above application under the provisions of 37 C.F.R. § 1.97 and § 1.98. The listed document was cited during prosecution of the corresponding PCT application. Enclosed are Form PTO-1449 and a copy of the listed document.

Respectfully submitted,



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